

In the Claims:

Please amend the claims as follows:

1. (currently amended) A pulse modulator comprising:
a delay arrangement for receiving a first regular sequence of pulses and for delaying each received pulse several times to obtain a plurality of regular sequences of pulses having different phases, wherein said delay arrangement comprises a shift register; and
a selection component for receiving from said delay arrangement a plurality of regular sequences of pulses having different phases, for receiving a modulating signal, wherein each possible value of said modulating signal is associated to one of said different phases, for selecting for each pulse of said first regular sequence of pulses a pulse of the respective regular sequence of pulses which sequence of pulses has a phase associated to a current value of said modulating signal, and for outputting said selected pulse as part of a pulse position modulated sequence of pulses.
2. (original) A pulse modulator according to claim 1, wherein said selection component is a multiplexer.
3. (cancelled)
4. (original) A pulse modulator according to claim 1, wherein said delay arrangement comprises a synchronization input for enabling a synchronization of delays applied by said delay arrangement to received pulses by means of a clock signal applied to said synchronization input.
5. (currently amended) A pulse modulator ~~according to claim 1,~~ comprising:
a delay arrangement for receiving a first regular sequence of pulses and for delaying each received pulse several times to obtain a plurality of regular sequences of pulses having different phases; and

a selection component for receiving from said delay arrangement a plurality of regular sequences of pulses having different phases, for receiving a modulating signal, wherein each possible value of said modulating signal is associated to one of said different phases, for selecting for each pulse of said first regular sequence of pulses a pulse of the respective regular sequence of pulses which sequence of pulses has a phase associated to a current value of said modulating signal, and for outputting said selected pulse as part of a pulse position modulated sequence of pulses; and

further comprising a pulse generator for generating a regular sequence of pulses and for providing said generated regular sequence of pulses as ~~[[a]]~~ the first regular sequence of pulses to said delay arrangement and in addition as a clock signal to said selection component.

6. (original) A pulse modulator according to claim 5, wherein said pulse generator further provides said generated regular sequence of pulses as a clock signal to said selection component.
7. (original) A pulse modulator according to claim 5, wherein said pulse generator generates said pulses with a frequency which is equal to the frequency with which values of said modulating signal are provided to said selection component.
8. (original) A pulse modulator according to claim 5, wherein said pulse generator comprises a control input for adapting the frequency of generated pulses to a frequency employed for said modulating signal which is provided to said selection component.
9. (original) A pulse modulator according to claim 1, wherein said delay arrangement comprises a control input for adjusting delays applied by said delay arrangement to received pulses in accordance with a frequency employed for said modulating signal which is provided to said selection component.

10. (original) A pulse modulator according to claim 4, further comprising a clock signal generator for generating said clock signal which is applied to said synchronization input of said delay arrangement, wherein said clock signal generator comprises a control input for adjusting the frequency of said clock signal in accordance with a frequency employed for a modulating signal which is provided to said selection component.
11. (original) A pulse modulator according to claim 1, further comprising a circuit for converting said pulse position modulated sequence of pulses output by said selection component into a corresponding pulse width modulated sequence of pulses.
12. (currently amended) A modulating system comprising a pulse modulator, which pulse modulator includes:
 - a delay arrangement for receiving a first regular sequence of pulses and for delaying each received pulse several times to obtain a plurality of regular sequences of pulses having different phases, wherein said delay arrangement comprises a shift register; and
 - a selection component for receiving from said delay arrangement a plurality of regular sequences of pulses having different phases, for receiving a modulating signal, wherein each possible value of said modulating signal is associated to one of said different phases, for selecting for each pulse of said first regular sequence of pulses a pulse of the respective regular sequence of pulses which sequence of pulses has a phase associated to a current value of said modulating signal, and for outputting said selected pulse as part of a pulse position modulated sequence of pulses.
13. (currently amended) A method of generating a modulated sequence of pulses, said method comprising the steps of:
 - generating a first regular sequence of pulses;
 - delaying via a shift register each of said generated pulses several times to obtain a plurality of regular sequences of pulses having different phases, wherein each possible value of a provided modulating signal is associated to one of said different phases;

selecting for each pulse of said first regular sequence of pulses a pulse of the respective regular sequence of pulses which sequence of pulses has a phase associated to a current value of said modulating signal; and

providing a respectively selected pulse as part of a pulse position modulated sequence of pulses.

14. (original) A method according to claim 13, wherein the delays which are applied to said generated pulses are synchronized by a clock signal.
15. (original) A method according to claim 13, wherein said pulses of said first regular sequence of pulses are generated with a frequency which is equal to the frequency with which values of said modulating signal are provided.
16. (original) A method according to claim 13, wherein said pulses of said first regular sequence of pulses are generated with a frequency which is adapted to a frequency employed for said provided modulating signal.
17. (original) A method according to claim 13, wherein the delays which are applied to said generated pulses are adjusted in accordance with said frequency employed for said provided modulating signal.
18. (original) A method according to claim 14, wherein the frequency of said clock signal used for said synchronization is adjusted in accordance with a frequency employed for said provided modulating signal.
19. (original) A method according to claim 13, further comprising converting said provided pulse position modulated sequence of pulses into a corresponding pulse width modulated sequence of pulses.

20. (new) A modulating system comprising a pulse modulator, which pulse modulator includes:

a delay arrangement for receiving a first regular sequence of pulses and for delaying each received pulse several times to obtain a plurality of regular sequences of pulses having different phases;

a selection component for receiving from said delay arrangement a plurality of regular sequences of pulses having different phases, for receiving a modulating signal, wherein each possible value of said modulating signal is associated to one of said different phases, for selecting for each pulse of said first regular sequence of pulses a pulse of the respective regular sequence of pulses which sequence of pulses has a phase associated to a current value of said modulating signal, and for outputting said selected pulse as part of a pulse position modulated sequence of pulses; and

a pulse generator for generating a regular sequence of pulses and for providing said generated regular sequence of pulses as the first regular sequence of pulses to said delay arrangement and in addition as a clock signal to said selection component.

21. (new) A method of generating a modulated sequence of pulses, said method comprising the steps of:

generating via a pulse generator a regular sequence of pulses;

providing said generated regular sequence of pulses as a first regular sequence of pulses;

delaying each of said generated pulses several times to obtain a plurality of regular sequences of pulses having different phases, wherein each possible value of a provided modulating signal is associated to one of said different phases;

selecting for each pulse of said first regular sequence of pulses a pulse of the respective regular sequence of pulses which sequence of pulses has a phase associated to a current value of said modulating signal, wherein said selecting receives said generated regular sequence of pulses as a clock signal; and

providing a respectively selected pulse as part of a pulse position modulated sequence of pulses.